



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JAN 8 1997
In re patent application of
K. Kata et al.
Serial No.: 08/533,207

Filed: September 25, 1995

Group Art Unit: 2503

Examiner: Unknown

For: PROCESS FOR MANUFACTURING SEMICONDUCTOR DEVICE
AND SEMICONDUCTOR WAFER

Commissioner of Patents and Trademarks
Washington, D.C. 20231

RECEIVED

JAN 09 1997

GROUP 2500

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 C.F.R. 1.97 through 1.99 and pursuant to applicant's duty of disclosure under 37 C.F.R. 1.56, applicant respectfully brings the following documents, cited in the European Search Report and listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

I hereby certify that each item of information contained in this Information Disclosure Statement was cited in a communication received from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

#5/IDS
1-15-97
VK/bs

WN-1439

Please charge any deficiencies in fees and credit any overpayment of fees to
Attorney's Deposit Account No. 23-1951.

Respectfully submitted,



Sean M. McGinn
Reg. No. 34,386

Whitham, Curtis, Whitham & McGinn
Intellectual Property Law
Reston International Center
11800 Sunrise Valley Drive
Suite 900
Reston, Virginia 20191
(703) 391-2510